A scalable algorithm for homomorphic computing on multi-core clusters

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Outline

- Introduction
- 2 Homomorphic programming
- 3 BSP execution of boolean circuits
- 4 Conclusion

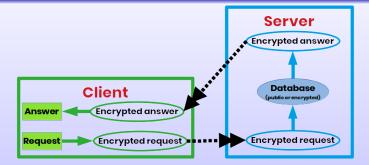
Outline

Introduction 00000

- Introduction

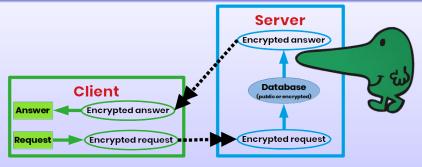
What is the context?

Introduction 00000



Q: What is the context?

Introduction 00000

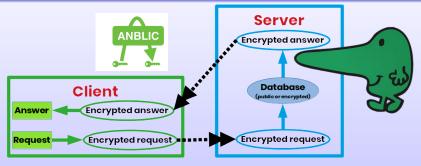


Too-curious-but-honest server (cloud provider):

- Personal data (e. g. medical; health pass)
- Compagny data (medical data or secret formula)

Q: What is the context?

Introduction 00000



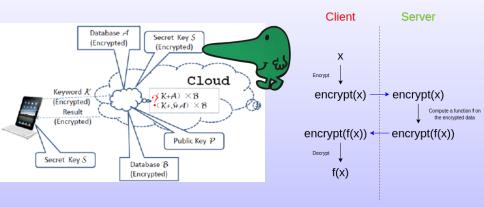
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ANBLIC project (Analysis in BLInd Clouds), funded by FUI/BPI with ATOS, Capgemini, CoESSI, Wallix, CEA, ENS, UPEC

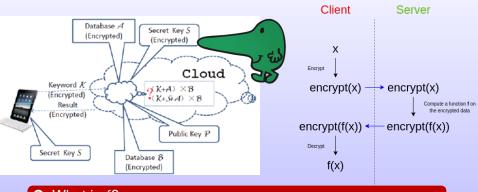
Q: More specifically?

Introduction



Q: More specifically?

Introduction



Q: What is *f*?

Homomorphic programming is computationally universal

But highly inefficient

•: What is homomorphic encryption?

A homomorphic operator (+, ⊕)

Introduction

- If $Enc(m_i)=c_i$ and $Dec(c_i)=m_i$ $(i \in \{1,2\})$
- Then $Dec(c_1 \oplus c_2) = Dec(c_1) + Dec(c_2) = m_1 + m_2$

What is homomorphic encryption?

A homomorphic operator $(+, \oplus)$

Introduction

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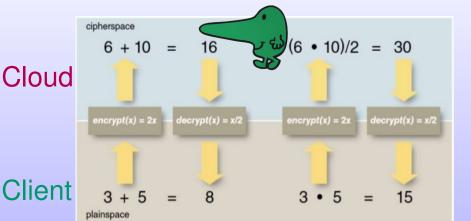
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homomorphic encryption (FHE)

FHE is when all the functions (Turing completness) can be evaluated in a homomorphic way. Mainly, using boolean circuits (multiplicative depth problem).

Come examples?

Introduction



Q: Some examples?

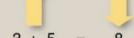
cipherspace
$$6 + 10 = 16$$
 $(6 \cdot 10)/2 = 30$

Problem and additional solution

- Problem: very inefficient programs (an example later)
- Solution: have executed them on HPC architectures (parallel and distributed ones)

Client

Introduction 00000



$$3 + 5 = 8$$

: Some examples?



Cloud

Introduction 00000

Needs of a bridging model (to scale and be portable)

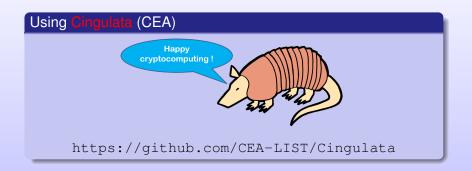
BSP

plainspace

Outline

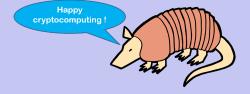
- 2 Homomorphic programming

: How FHE programming?



: How FHE programming?

Using Cingulata (CEA)



https://github.com/CEA-LIST/Cingulata

Cingulata C++ environment

- Compilation chain (specific types)
- C++ operator overloading capabilities
- Generation of (encrypted) boolean circuits

Can you show a simple example?

Sum of two integers!

C: Can you show a simple example?

```
#include <*.hxx>
   int main() {
     CiContext::set_config(...); // Set context
3
4
     Cilnt a{Cilnt::u8}; // create from unsigned 8-bit template
5
     Cilnt b{Cilnt::u8};
6
     Cilnt c{Cilnt::u8};
7
8
     a.read("a"); // read variable a and set name
9
     b.read("b"); // read variable a and set name
10
     c = a + b:
11
     c.write("c"); // set name and write variable
12
13
     /* Export to file the "tracked" circuit */
14
     CiContext::get bit exec t<BitTracker>()->export blif(blif name, "hello")
15
16
```

Conclusion

Can you show a real example?

Membership in an encrypted data-base!

Can you show a real example?

```
1 int main() {
      CiInt PassTarget(CiInt::s32);
2
      vector<Cilnt> HeathPass(list_size, Cilnt::s32);
3
      PassTarget.read("a");
4
      // Initialising the Data Base (reading) as an array
5
      for (int i = 0; i < list size; i++) {
6
         HeathPass[i].read("b_" + to_string(i));
7
8
      // Comparing all the data (while updating the answer)
9
      bool answer = false;
10
      for (int i = 0; (i < list size) or (not(answer)); i++) {
11
         answer = (PassTarget == HeathPass[i]);
12
13
      // Finally writing the answer
14
      answer.write("s");
15
16
```

Conclusion

• Can you show a real example?

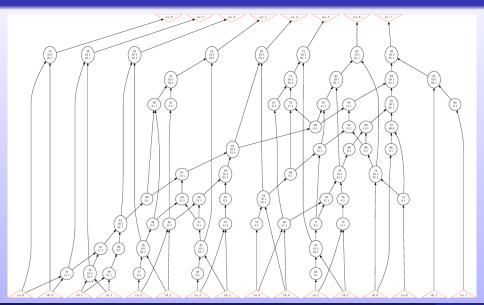
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6
         HeathPass[i].read("b " + to string(i));
7
8
      // Comparing all the data (while updating the answer)
9
      CiBit answer(0);
10
      for (int i = 0; i < list size; i++) {
11
         answer = answer xor (PassTarget == HeathPass[i]);
12
13
      // Finally writing the answer
14
      answer.write("s");
15
16
```

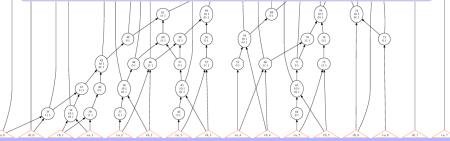








- Cingulata generates boolean circuits
- They are big but also manipulating encrypted booleans
- Making their execution very slow
- Even on multi-cores using parallel threads





What can we remark?

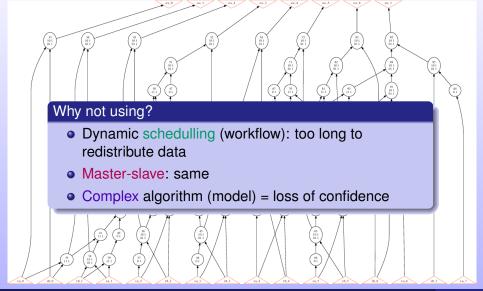
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What can we do?

- Distributed execution using the BSP model
- Finding a distribution of the gates
- Benchmarking







Execution BSP •0000000000

Outline

- BSP execution of boolean circuits

Q: How to manage the gates?

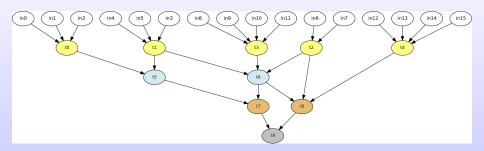
Levels (v is a gate of G)

level(G, v) is inductively defined by :

- 0 if $v \in V_{in}$ (input)
- $\max(n_i) + 1$ if $\forall v_i$ such that $v \in \mathbf{edges}(G, v_i)$ and **level**(G, v_i) = n_i (otherwise)

Execution BSP 0000000000

Q: How to manage the gates?



Execution BSP ·•000000000

Example

Niveau ₀	$[\mathit{in}_0, \mathit{in}_1, \ldots, \mathit{in}_{15}]$
Niveau ₁	$[t_0, t_1, t_3, t_2, t_4]$
Niveau ₂	$[t_5, t_6]$
Niveau ₃	$[t_7, t_8]$
Niveau ₄	[t ₉]

Q: What is the BSP execution?

Algorithm: BSP execution of boolean circuits

```
input: A boolean circuit G and encrypted input bits
   output: Encrypted output bits
   (G, levels, N) \leftarrow build(G);
   \{G_i\} \leftarrow \mathbf{select}(G, levels, N, \mathbf{pid});
   bits \leftarrow \mathbf{read}(V_{in});
   for l \in \{1 \cdots N\} do
          sub \leftarrow G_i where G_i \in \{G_i\}:
7
          toSend \leftarrow run(sub, bits);
          rcv ← CommBSP(toSend);
9
          bits \leftarrow update(rcv);
10
   end
   write(V_{out}, bits);
```

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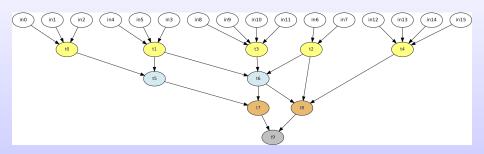
end

write(V_{out}, \mathbf{bits});
```

Distribution of the gates

Hashing the gates modulo p

Q: What is the BSP execution?



Example of distribution (BSP machine with $\mathbf{p} = 2$)

Niveau ₁	$[t_0 \to 0, t_1 \to 1, t_3 \to 0, t_2 \to 1, t_4 \to 0]$
Niveau ₂	$[t_5 \to 0, t_6 \to 1]$
Niveau ₃	$[t_7 \to 0, t_8 \to 1]$
Niveau ₄	$[t_9 \rightarrow 0]$

Q: What is your distributed architecture?

Benchmarks on Grid'5k (Grid'5000)



8 places, 38 clusters, 761 nodes, 15696 CPU cores, 101.5 TiB RAM

What is your distributed architecture?



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Execution BSP 0000000000

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Benchmarks on Grid'5k (CGrid'5000)



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Execution BSP 0000000000

What is your distributed architecture?

Benchmarks on Grid'5k (* Grid'5000)





8 places, 38 clusters, 761 nodes, 15696 CPU cores. 101.5 TiB RAM

Execution BSP

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Machines

- Lille/chetemi; 15 nodes of 2×Intel Xeon E5-2630 of 10 cores (with 256 GiB RAM) and a 10 Gbps (SR-IOV) network
- Nantes/ecotype; 48 nodes of 2×Intel Xeon E5-2630 of 10 cores (with 128 GiB) and a 10 Gbps (SR-IOV) network

Execution BSP 0000000000

: For which performances?

Benchmarks of membership

#size	#nodes	#procs	#time(s)	speedup
80	4	2	22	3.6
80	16	2	12	6.7
160	4	2	66	2.6
160	16	2	22	7.8
320	4	2	97	-
320	16	2	41	-

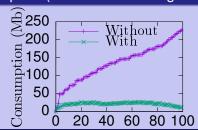
Execution BSP 0000000000

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Memory consumption (with/without managment)



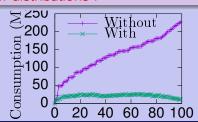
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Not enough!

- Too much and badly balanced communication
- Badly balanced computations
- We need better distributions!



Q: How to find such distributions?

NP-Hard problem

So, need of an heuristic

Generic genetic algorithm Génération intermédiaire Génération t (mating pool) Génération t+1 Sélection Croisements Mutations...

Generic to Instantiation

Execution BSP 00000000000

Generic to Instantiation

- Individuals ≡ association gates ⇒ processors (per level)

BSP Execution of Homomorphic Programs

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- Mutation ≡ moving a gate in the association

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- But working only for a single machine for each circuit

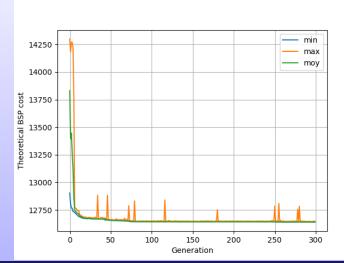
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- Easy to implement and local optimum
- Partial results can be reused
- But working only for a single machine for each circuit
- But "slow" so only for the biggest circuits

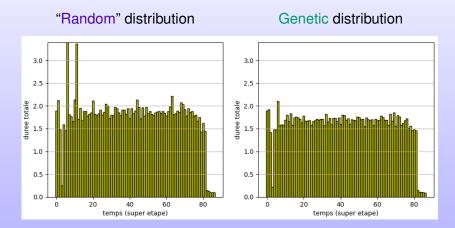
: How much time to find such local optimums?

Membership (80 items) for a 2 × 4 processors machine:



Q: Do we gain in load-balancing?

For each super-step (membership 40 items):



Execution BSP

Execution BSP

: Have you another example?

From the ANBLIC project

Public medical database and encrypted requests (scoring):

$$score(q, d) = coord(q, d) \times \sum_{t \in q} tf(t, d) \times idf(t)^{2}$$

Could be rewrite as:

$$score(q, d) = \sum_{t \in T} x_t^{(q)} . \chi(t, d) \times \sum_{t \in T} x_t^{(q)} . \alpha(t, d)$$

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```

: Have you benchmarks?

Benchmarks of scoring (request in a medical database)

#nodes	#procs	#cores	#BSP-naive	#BSP-gen	speedup
1	1	16	-	5769	-
4	1	16	5408	3603	1.6
4	2	8	2485	1866	3.1
8	1	16	3432	1958	3.0
8	2	8	1390	1148	5.0
16	1	16	1310	1095	5.3
16	2	8	716	651	8.9

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Not enough!

- Better performances using the "genetic" distribution
- Better performances when using both multi-core and BSP
- But not a linear acceleration

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Introduction

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Main results

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- (Multi-)BSP execution
- Merging of sub-graphs (sub-distribution) to avoid some synchronisations and having better performances
- More examples and benchmarks

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Merci!